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| TO: | Examiner Jonathan J. JOHNSON | FROM: | Stephen T. Neal |
| COMPANY: | USPTO | DATE: | August 10, 2005 |
| FAX NUMBER: | (571) 273-8300 | TOTAL NO. OF PAGES INCLUDING COVER: | 8 |
| PHONE NUMBER: | | SENDER'S REFERENCE NUMBER: | Intel 2207/1266902 |
| RE: | Serial No.: 10/612,804 | YOUR REFERENCE NUMBER: | Group Art Unit: 1725 |

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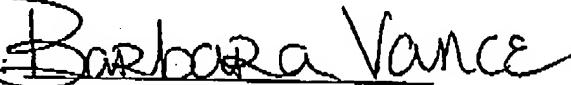
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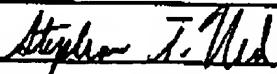
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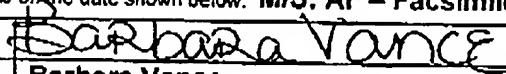
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| TRANSMITTAL FORM | | Application Number | 10/612,804 |
| | | Filing Date | July 1, 2003 |
| | | First Named Inventor | Terry STERRETT et al. |
| | | Art Unit | 1725 |
| | | Examiner Name | Jonathan J. JOHNSON |
| Total Number of Pages in this Submission | 8 | Attorney Docket Number | Intel 2207/1266902 |

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| <input checked="" type="checkbox"/> Amendment / Reply | <input type="checkbox"/> Petition | <input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief) |
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| Firm | Kenyon & Kenyon |
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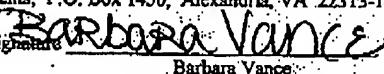
Attorney Docket No.: Intel 2207/1266902
 Assignee: Intel Corporation
 Response Under 37 C.F.R. §1.116
 Expedited Procedure
 Examining Group 1725

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Terry STERRETT et al.
 SERIAL NO. : 10/612,804
 FILED : July 1, 2003
 FOR : A METHOD AND APPARATUS FOR IMPROVING
 AN INTEGRATED CIRCUIT DEVICE
 GROUP ART UNIT : 1725
 EXAMINER : Jonathan J. JOHNSON

VIA FACSIMILE

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| Dated: August 10, 2005 | Signature:  |
| Barbara Vance | |

AMENDMENT AND RESPONSE

Dear Sir:

The following amendment and remarks below are respectfully submitted in response to the Office Action dated June 10, 2005.

Amendments to the Claims are reflected in the listing of claims which begin on page 2 of this paper.

Remarks/Arguments begin on page 5 of this paper.